

FORM PTO-892 (REV. 2-92)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		SERIAL NO. <b>09-683863 2817</b>		GROUP/ART UNIT <b>2817</b>		ATTACHMENT TO PAPER NUMBER <b>13</b>			
NOTICE OF REFERENCES CITED				APPLICANT(S) <b>McGrath</b>							
U.S. PATENT DOCUMENTS											
•		DOCUMENT NO.				DATE	NAME	CLASS	SUB-CLASS	FILING DATE IF APPROPRIATE	
	A	<b>6344663</b>				<b>25-2002</b>	<b>Slater, Jr et al.</b>	<b>257</b>	<b>77</b>		
	B										
	C										
	D										
	E										
	F										
	G										
	H										
	I										
	J										
	K										
FOREIGN PATENT DOCUMENTS											
•		DOCUMENT NO.				DATE	COUNTRY	NAME	CLASS	SUB-CLASS	PERTINENT SHTS. DWG. PP. SPEC.
	L	<b>58130608</b>				<b>8-4-1983</b>	<b>Japan</b>	<b>Onishi et al.</b>	<b>—</b>	<b>—</b>	
	M										
	N										
	O										
	P										
	Q										
OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)											
	R	<b>Palmour et al. "High-temperature depletion-mode metal-oxide-semiconductor field-effect transistors in beta-SiC thin films" Appl. Phys. Lett. 51, 14 December 1987 pp 2028-2030</b>									
	T	<b>Schmid et al. "Process technology and high temperature performance of 6H-SiC MOS devices" The Third European Conference on High temperature Electronics 1999 pp 195-199</b>									
EXAMINER		DATE									
<b>MICHAEL B SHINGLETON</b>		<b>4-3-03</b>									
* A copy of this reference is not being furnished with this office action. (See Manual of Patent Examining Procedure, section 707.05 (a).)											